



Govt. of Bihar
MUZAFFARPUR INSTITUTE OF TECHNOLOGY,
MUZAFFARPUR, BIHAR – 842003
(Under the Department of Science & Technology, Govt. of Bihar, Patna)

Notice. No.: 60/TEQIP-III

Dated: 25/09/2018

Rebid for NT1 Lab

M.I.T. Muzaffarpur is in the process of procuring equipment of Electrical Engineering Department under TEQIP-III Scheme. Detail specification are as follows.

Package Name: MITM/EE/NT1 LAB

Package Code: TEQIP-III/BH/mitm/92

Interested supplier/bidders are required to give the following information/documents on principal@mitmuzaffarpur.org latest by 30.09.2018 so that invitation for bidding process can be initiated through PMSS.

1. Supplier Name :
2. Address(With Pin Code):
3. Contact Person Name :
4. E-Mail-ID:
5. Mobile No.:
6. TIN/GST No.
7. PAN No.

Sr. No	Item Specifications	Remark
1	Electrical and Electronic System Trainer Specification <ul style="list-style-type: none">•Built in Power Supply<ul style="list-style-type: none">•DC Supply: 5V / 1A. & ± 12V, 500mA.0 to 15V DC (Variable), 100 mA (Isolated),0 to 30V DC (Variable), 100 mA (Isolated),High Volt DC -15V to 110V, 100mA.•AC Supply: 12-0-12V AC,150 mA. Short circuit Protected.•Built in Function Generator<ul style="list-style-type: none">O/p Waveform : Sine, Triangle & TTL OOutput Frequency:1 Hz to 1MHz in 6 ranges, with amplitude & frequency control pots. O/P Voltage 20Vp-p max. (Sin/TRG)Modulation I/PAM : - I/P voltage + 5V (100% modulation) O/P - For 0V (min), + 5V (max.) - 5V (Phase reversal of O/P) FM : I/P voltage ± 400mV (+ 50% modulation)•Clock Generator : 10 MHz TTL clock.•Data Switches (10 No.) & bi-colour LED status indicators 10X2 Nos, for High/Low indication.•Pulser switches (2 Nos.) with four debounced outputs - 2No.•Optional BNC to 2 channel banana adapter - 2No.•Logic probe to detect High/Low level pulses upto 1MHz, with bi-colour LEDs to indicate status.	02

	<ul style="list-style-type: none"> •2 / 4 digit 7 segment display with BCD to 7 segment decoder. •Onboard DPMS mode/range selection. (A) DC volt: 2V/200V - 1No. B) DC current: 2mA/200mA - 1No. (C) DC Volts/Current : 20V/200mA - 1No. •Onboard moving iron meters provided for (A) AC Current: 1 AMP - 1No. (B) AC Voltage : 15V - 1No. •Onboard speaker: 8 Ohms, 0.5 Watt (1No.) •Onboard POTS : 1K - 1No. 1M - 1No. •Operating Voltage: 220/240Vac switch settable $\pm 10\%$, 50Hz/72VA. 	
2	<p>To plot the frequency response of Low pass filter and determine the half power frequency To study frequency response of Band pass filter</p> <p>Electrical and Electronic System Trainer</p> <p>Specification</p> <ul style="list-style-type: none"> •Built in Power Supply •DC Supply: 5V / 1A. & $\pm 12V$, 500mA. 0 to 15V DC (Variable), 100 mA (Isolated), 0 to 30V DC (Variable), 100 mA (Isolated), High Volt DC -15V to 110V, 100mA. •AC Supply: 12-0-12V AC, 150 mA. Short circuit Protected. •Built in Function Generator O/p Waveform : Sine, Triangle & TTL O Output Frequency: 1 Hz to 1MHz in 6 ranges, with amplitude & frequency control pots. O/P Voltage 20Vp-p max. (Sin/TRG) Modulation I/PAM : - I/P voltage + 5V (100% modulation) O/P - For 0V (min), + 5V (max.) - 5V (Phase reversal of O/P) FM : I/P voltage $\pm 400mV$ (+ 50% modulation) •Clock Generator : 10 MHz TTL clock. •Data Switches (10 No.) & bi-colour LED status indicators 10X2 Nos, for High/Low indication. •Pulser switches (2 Nos.) with four debounced outputs - 2No. •Optional BNC to 2 channel banana adapter - 2No. •Logic probe to detect High/Low level pulses upto 1MHz, with bi-colour LEDs to indicate status. •2 / 4 digit 7 segment display with BCD to 7 segment decoder. •Onboard DPMS mode/range selection. (A) DC volt: 2V/200V - 1No. B) DC current: 2mA/200mA - 1No. (C) DC Volts/Current : 20V/200mA - 1No. •Onboard moving iron meters provided for (A) AC Current: 1 AMP - 1No. (B) AC Voltage : 15V - 1No. •Onboard speaker: 8 Ohms, 0.5 Watt (1No.) •Onboard POTS : 1K - 1No. 1M - 1No. •Operating Voltage: 220/240Vac switch settable $\pm 10\%$, 50Hz/72VA. 	01

Note: Detailed specification of all items will be attached in the invitation letter.

Faiz Ahmad
25/09/18

[Signature]
25/09/18
Principal
MIT Muzaffarpur